

Docket No.: 043876-0161



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Craig HANSEN, et al.	:	Confirmation Number: 5532
	:	
Application No.: 10/757,836	:	Group Art Unit: 2183
	:	
Filed: January 16, 2004	:	Examiner: Coleman, Eric
	:	
For: METHOD AND SOFTWARE FOR PARTITIONED FLOATING-POINT MULTIPLY-ADD OPERATIONS		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

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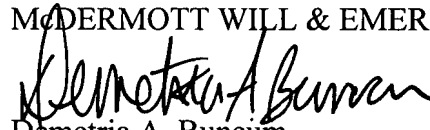
Dear Sir:

In accordance with 37 CFR §1.97, §1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached from PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Please note that also included on the PTO-1449 is a patent family tree for U.S. patent no. 5,742,840, and an Office Action dated February 21, 2007 which was issued in related U.S. patent application no. 10/757,851, and an amendment dated August 21, 2007 filed in response thereto.

Please charge any shortage of fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please fully credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



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Registration No. 58,848

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**Date: September 10, 2007**

**Please recognize our Customer No. 20277  
as our correspondence address.**



□ □

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Substitute for form 1449A/PTO			<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Application Number	10/757,836
			Filing Date	January 16, 2004
			First Named Inventor	Craig Hansen
			Group Art Unit	2183
			Examiner Name	Coleman, Eric
			Attorney Docket Number	43876-161
Sheet	1	of	4	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A	US-4,509,119	04-02-1985	Gumaer	
	B	US-4,943,919	07-24-1990	Aslin	
	C	US-5,208,914	05-04-1993	Wilson	
	D	US-5,253,342	10-12-1993	Blount	
	E	US-5,256,994	10-26-1993	Langendorf	
	F	US-5,287,327	02-15-1994	Takasugi	
	G	US-5,327,570	07-05-1994	Foster	
	H	US-5,371,772	12-06-1994	Al-Khairi	
	I	US-5,375,208	12-20-1994	Pitot	
	J	US-5,477,181	12-19-1995	Li	
	K	US-5,522,054	05-28-1996	Gunlock	
	L	US-5,530,960	06-25-1996	Parks	
	M	US-5,826,106	10-20-1998	Pang	
	N	US-5,828,869	10-27-1998	Johnson	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	O	WO 97/07450	02-27-1997	Hansen		

Examiner Signature		Date Considered	
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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	P	11/2/05 Complaint for Patent Infringement ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Q	2/16/06 SCEA's Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	R	3/1/06 MicroUnity's Opposition to Defendant's Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	S	3/1/06 MicroUnity's Answer to SCEA's Counterclaim ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	T	3/6/06 SCEA's Reply Supporting Its Motion for Stay of Litigation Pending Reexamination ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	U	9/29/06 Order Denying Motion to Continue, Finding as Moot Motion to Stay ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	V	12/1/06 MicroUnity's Disclosure of Asserted Claims And Preliminary Infringement Contentions Pursuant to Local Patent Rule 3-1 and Attachments A-Z, AA, and BB ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	W	12/1/06 MicroUnity's Identification of Documents Pursuant to Local Patent Rule 3-2 ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	X	12/20/06 MicroUnity's Initial Disclosures ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Y	12/21/06 SCEA's Initial Disclosures Pursuant to the Discovery Order ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	Z	1/25/07 First Amended Complaint for Patent Infringement and Exhibits A-J ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AA	2/12/07 SCEA's Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	

Examiner Signature		Dated Considered	
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>			<i>Complete if Known</i>		
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			Filing Date	January 16, 2004	
			First Named Inventor	Craig Hansen	
			Group Art Unit	2183	
			Examiner Name	Coleman, Eric	
Sheet	3	of	4	Attorney Docket Number	43876-161

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AB	3/2/07 MicroUnity's Answer to SCEA's Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AC	3/5/07 SCEA's Invalidity Contentions & Attachments A-I ( <b>portions of G and H are Outside Counsel Eyes Only and are not included</b> ) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AD	4/3/07 SCEA's Supplemental Disclosures ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AE	5/1/07 SCEA's Supplemental Invalidity Contentions & Attachments A-I ( <b>Exhibits A2, E2, F2, G9 and H7 are Outside Counsel Eyes Only and are not included</b> ) ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AF	5/9/07 SCEA's Amended Answer, Affirmative Defenses, And Counterclaims to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AG	5/22/07 MicroUnity's LPR 4-1 Proposed List of Terms to be Construed ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AH	5/22/07 SCEA's Proposed List of Claim Terms, Phrases, Clauses, And Elements For Construction ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AI	5/29/07 MicroUnity's Answer to SCEA's Amended Counterclaim in Response to MicroUnity's First Amended Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Sony Computer Entertainment America, Inc.</i> ; 2:05-cv-505; USDC for the Eastern District of Texas, Marshall Division)	
	AJ	11/22/06 Complaint Against Advanced Micro Devices, Inc. and Exhibits A-L ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AK	2/26/07 AMD's Answer to MicroUnity's Complaint ( <i>MicroUnity Systems Engineering, Inc. v. Advanced Micro Devices, Inc.</i> ; 2:06-cv-486; USDC for the Eastern District of Texas, Marshall Division)	
	AL	Advance Datasheet for MT48LC2M8S1(S) 2 Meg x 8 SDRAM, Micron Semiconductor, Inc., 2-43 to 2-84 (1994)	

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	AM	Asprey et al., "Performance Features of the PA7100 Microprocessor," IEEE Micro, 22-35 (June 1993)	
	AN	BSP and BSP Customer Attributes, Inclosure 5, Burroughs Corporation (Aug. 1, 1977)	
	AO	BSP Floating Point Arithmetic, Burroughs Corporation, (Dec. 1978)	
	AP	BSP Implementation of Fortran, Burroughs Corporation (Feb. 1978)	
	AQ	BSP, Burroughs Scientific Process, Burroughs Corporation, 1-29 (June 1977)	
	AR	Bursky, "Synchronous DRAMs Clock At 100 MHz," Electronic Design, Vol. 41, No. 4, 45-49 (Feb. 18, 1993)	
	AS	D. D. Gajski and L. P. Rubinfeld, "Design of Arithmetic Elements for Burroughs Scientific Processor," Proceedings of the 4th Symposium on Computer Arithmetic, Santa Monica, CA, 245-56 (1978)	
	AT	"System Architecture," ELXSI (2d Ed. Oct. 1983)	
	AU	"System Foundation Guide," ELXSI (1st Ed. Oct. 1987)	
	AV	L. Higbie, "Applications of Vector Processing," Computer Design, 139-45 (Apr. 1978)	
	AW	Ide et al., "A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors," IEEE Journal of Solid-State Circuits, Vol. 28, No.3, 352-61 (Mar. 1993)	
	AX	"IEEE Standard for Communicating Among Processors and Peripherals Using Shared Memory (Direct Memory Access—DMA)," IEEE (1994)	
	AY	D.A. Kuck & R. Stokes, "The Burroughs Scientific Processor (BSP)," IEEE Transactions on Computers, vol. C-31, No. 5, 363-76 (May 1982)	
	AZ	Lee, "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, Vol. 15, No.2, 22-32 (Apr. 1995)	
	BA	Lion Extension Architecture (Oct. 12, 1991)	
	BB	"MC88110 Second Generation RISC Microprocessor User's Manual," Motorola (1991)	
	BC	Spaderna et al., "An Integrated Floating Point Vector Processor for DSP and Scientific Computing," IEEE International Conference on Computer Design: VLSI in Computers and Processors, 8-13 (Oct. 1989)	
	BD	Sprunt et al., "Priority-Driven, Preemptive I/O Controllers for Real-Time Systems," IEEE (1988)	
	BE	Thornton, "Design of a Computer-The Control Data 6600," Scott, Foresman and Company (1970)	
	BF	Watkins et al., "A Memory Controller with an Integrated Graphics Processor," IEEE, 324-36 (1993)	
	BG	Patent Family Tree of Patent 5,742,840	
	BH	Office Action dated February 21, 2007 received in related Patent Application No. 10/757,515	
	BI	Amendment filed August 21, 1007 in response to Office Action dated Feb. 21, 2007 in related Patent Application No. 10/757,515	

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